Identification des défauts par la méthode de l'analyse de la signature

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Abstract

The high complexity of systems, the reduction of their fabrication and exploitation costs makes their utilization more and more important. Therefore, the assurance of functionality is nowadays a vital domain in the development of systems in the industrial field. Faults generation methods are, in general, by analog nature – the "V/I" test – or by logical nature – a succession of bits where is applied a rule: AND, OR, XOR (OR exclusive) etc. – generated under the form of a state or a parametric signature.

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